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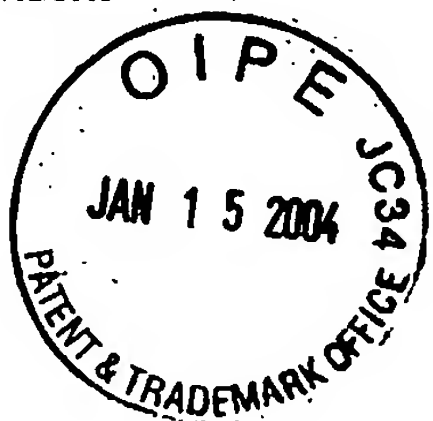
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25944 7590 11/12/2003

OLIFF & BERRIDGE, PLC  
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| (Depositor's name) |
| (Signature)        |
| (Date)             |

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/052,255      | 01/23/2002  | Akihiko Ebina        | 111778              | 1930             |

TITLE OF INVENTION: METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING NONVOLATILE SEMICONDUCTOR MEMORY DEVICES

| APPLN. TYPE    | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE   |
|----------------|--------------|-----------|-----------------|------------------|------------|
| nonprovisional | NO           | \$1330    | \$300           | \$1630           | 02/12/2004 |

| EXAMINER       | ART UNIT | CLASS-SUBCLASS |
|----------------|----------|----------------|
| GEYER, SCOTT B | 2829     | 438-257000     |

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## 3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

SEIKO EPSON CORPORATION

TOKYO, JAPAN

HALO LSI DESIGN & DEVICE TECHNOLOGY, INC.

WAPPINGER FALLS, NEW YORK

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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- ☒ A check in the amount of the fee(s) is enclosed. Check no. 150134 (\$1630)
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(Authorized Signature)

(Date)

Ian R. Vallejo, Reg. NO. 53,714 January 15, 2004

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01/16/2004 MGE BREM2 00000109 10052255

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